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Total Number of Pages: 02

Course: B.Tech  
Sub\_Code: REC3C001

3<sup>rd</sup> Semester Back Examination: 2024-25

SUBJECT: Analog Electronic Circuits

BRANCH(S): AEIE, BIOMED, ECE, EEE, ELECTRICAL, ELECTRONICS & C.E, ETC

Time: 3 Hours

Max Marks: 100

Q.Code: R594

Answer Question No.1 (Part-I) which is compulsory, any eight from Part-II and any two from Part-III.

The figures in the right hand margin indicate marks.

**Part-I**

**Q1 Answer the following questions:**

**(2 x 10)**

- Explain, how FETs are unipolar? Draw the V-I Characteristics of an E- MOSFET.
- Discuss the concept of load line and operating point of a BJT
- Draw and briefly explain a current mirror circuit using BJT.
- With suitable diagram, discuss the advantages of cascaded system using MOSFET.
- With  $g_m = 50 \text{ mA/V}$ ,  $r_{be} = 1 \text{ K}\Omega$ ,  $C_e = 1 \text{ pF}$ , and  $C_c = 0.2 \text{ pF}$ , determine the values of  $f_\beta$  and  $f_T$ .
- Differentiate between the negative and positive feedback circuits.
- Write a short note on class AB amplifier.
- Discuss the characteristics of an ideal Op-Amp.
- Write the other functions of differentiator and integrator circuits using OP-AMP.
- Determine  $S (V_{BE})$  for fixed-bias with  $R_B = 250 \text{ K}\Omega$  and  $\beta = 90$ .

**Part-II**

**Q2 Only Focused-Short Answer Type Questions- (Answer Any Eight out of Twelve)**

**(6 x 8)**

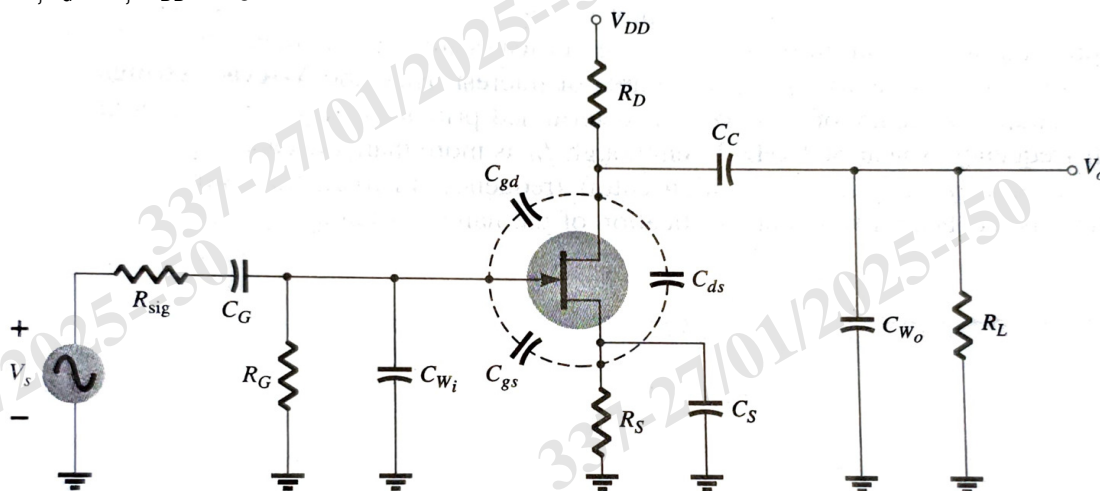
- Explain, why the self-bias is named so? Discuss its advantages.
- Discuss about the stability of DC bias with voltage feedback in comparison to the self-bias circuit. Determine the quiescent levels of  $I_C$  and  $V_{CE}$  for the voltage feedback biasing network with  $\beta = 90$ ,  $V_{CC} = 10\text{V}$ ,  $R_C = 4.7 \text{ K}\Omega$ ,  $R_B = 250 \text{ K}\Omega$ ,  $R_E = 1.2 \text{ K}\Omega$
- Explain the principle of operation of a P-Channel MOSFET.
- A MOSFET is to operate at  $I_D = 0.1 \text{ mA}$  and is to have  $g_m = 1 \text{ mA/V}$ . If  $k_n = 50 \mu\text{A/V}^2$ , find the required  $W/L$  ratio and the overdrive voltage.
- With a neat circuit diagram of a small signal analysis of BJT network, derive the expressions for  $Z_i$ ,  $Z_o$ ,  $A_i$ , and  $A_v$ .
- For a CS MOSFET amplifier circuit with  $V_{DD} = +15 \text{ V}$ ,  $V_t = 1.5 \text{ V}$ ,  $k'_n(W/L) = 0.25 \text{ mA/V}^2$ ,  $V_A = 50 \text{ V}$ ,  $R_G = 10 \text{ MOhm}$ ,  $R_D = 10 \text{ KOhm}$ ,  $R_L = 10 \text{ KOhm}$ , find  $I_D$ ,  $V_D$ ,  $g_m$ , and  $r_o$ .

- g) Consider a voltage divider biased amplifier circuit with  $V_{CC} = +20\text{ V}$ ,  $R_1 = 80\text{ K}\Omega$ ,  $R_2 = 40\text{ K}\Omega$ ,  $R_C = 15\text{ K}\Omega$ ,  $R_E = 5\text{ K}\Omega$ ,  $R_L = 30\text{ K}\Omega$ . The used transistor has the following set of h-parameters:  
 $h_{ie} = 2\text{ K}\Omega$ ,  $h_{fe} = 50$ ,  $h_{re} = 4 \times 10^{-4}$ ,  $h_{oe} = 25 \times 10^{-6}$  Siemens. Determine  $R_i(\text{base})$ ,  $R_i(\text{overall})$ ,  $r_o$ ,  $r_o$  (overall), and  $A_v$ .
- h) Find the high frequency response parameters of CS-amplifier with source resistance.
- i) Write short notes on the principles of sinusoidal oscillators and crystal oscillator circuits.
- j) State the Barkhausen criterion for sustained oscillations. Explain the principle of operation of a RC-phase shift oscillator circuit.
- k) Explain about any two non-inverting Configurations of OP-AMP circuits.
- l) Write in detail about any two compound configurations.

### Part-III

#### Only Long Answer Type Questions (Answer Any Two out of Four)

- Q3 Describe the three main biasing techniques used in biasing of MOSFET circuits. Extend the discussion on the stability of the techniques. Determine the dc bias voltage  $V_{CE}$  and the current  $I_C$  and the stability factor for the voltage divider biasing circuit with  $R_1 = 49\text{ K}\Omega$ ,  $R_2 = 4.9\text{ K}\Omega$ ,  $R_C = 20\text{ K}\Omega$ ,  $R_E = 2.5\text{ K}\Omega$ ,  $C_1 = C_2 = 10\text{ }\mu\text{F}$ , and  $\beta = 100$ . (16)
- Q4 Which capacitors affects the high frequency response of a BJT amplifier circuit. Describe in detail. Determine the high cut-off frequencies for the network using the following parameters:  
 $C_G = 0.01\text{ }\mu\text{F}$ ,  $C_C = 0.5\text{ }\mu\text{F}$ ,  $C_S = 2\text{ }\mu\text{F}$ ,  $C_{gd} = 2\text{ pF}$ ,  $C_{gs} = 4\text{ pF}$ ,  $C_{ds} = 0.5\text{ pF}$ ,  $C_{wi} = 5\text{ pF}$ ,  $C_{wo} = 6\text{ pF}$ ,  $R_{sig} = 10\text{ K}\Omega$ ,  $R_G = 1\text{ M}\Omega$ ,  $R_D = 4.7\text{ K}\Omega$ ,  $R_S = 1\text{ K}\Omega$ ,  $R_L = 2.2\text{ K}\Omega$ ,  $I_{DSS} = 8\text{ mA}$ ,  $V_P = -4\text{ V}$ ,  $r_d = \infty$ ,  $V_{DD} = 20\text{ V}$ . (16)



- Q5 Write notes on Class A, B, AB, C Power Amplifiers (16)
- Q6 With suitable diagram, explain in detail the single input balanced output differential circuit, Differentiator and Integrator using OP-AMP. (16)